IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT APPLICATION of

Inventor(s):

Inagaki et al.

Atty.Dkt:

41226/278095

09/830,360

Group Art Unit: Examiner.

2827

Tuan T. Dinh

Appln. No.: Filed:

April 25, 2001

Printed circuit board and method of manufacturing pritned circuit board

Date: July 8, 2003

IDS LETTER CITING APPLICATION(S)

Examiner's attention is directed to the following application(s) of:

Examiner's Initials*	FIRST INVENTOR (Last Name Only)	APPLICATION NO. & Filing Date	ENCLOSED
AN	Inagaki et al.	09/830,361 Apr 25, 2001	Spec S Drawings S Current Claims ☐ Other
			Spec Drawings Current Claims Other
			☐ Spec ☐ Drawings ☐ Current Claims ☐ Other

PLEASE DO NOT PRINT the above information on the patent which results from the subject application.

Consideration of each listed application is earnestly solicited since unpublished patent applications are contemplated as IDS material; see the exception in Rule 98(a)(2)(iii) and note the p nultimate sentence of MPEP 609.

Further, in keeping with MPEP 609, Subsec. C(2), 2nd para., line 10 to end of the paragraph (especially note lines 18-25) PLEASE RETURN A COPY OF THIS LETTER with the Examiner's initials adjacent each above listing so that applicant will know that each listed application has been considered as required by PTO policy. Thanks.

Respectfully submitted

GJP/mll

Pillsbury Winthrop LLP

Glenn J. Perry, Reg. No. 28,458

703.905.2000; fax: 703.905.2500

*The Examiner's initials adjacent a citation indicates he/she has considered the cited application relative to the subject application.

Tuan Dinh 7-23-03